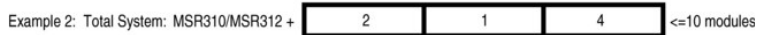


# Input And Output Module Selection

## Input Module Selection



# of inputs \ Groups	Groups		
	Group 1	Group 2	Group 3
1	MSR320	MSR320	MSR320
2	MSR320	MSR320	MSR320
3	MSR320	MSR320	MSR320
4	MSR320	MSR320	MSR320
5	MSR320	MSR320	MSR320
6	MSR320	MSR320	MSR320
7	MSR320	MSR320	MSR320
8	MSR320	MSR320	MSR320
9	MSR320	MSR320	MSR320
10	MSR320	MSR320	MSR320
11	MSR320	MSR320	MSR320
12	MSR320	MSR320	MSR320
13	MSR320	MSR320	MSR320
14	MSR320	MSR320	MSR320
15	MSR320	MSR320	MSR320
16	MSR320	MSR320	MSR320
17	MSR320	MSR320	MSR320
18	MSR320	MSR320	MSR320
19	MSR320	MSR320	MSR320
20	MSR320	MSR320	MSR320



**Note:** If a muting module is required in the application, it counts as one of the ten input modules.

## Output Module Selection

# of outputs	Groups			Delayed		
	Immediate Group 1	Immediate Group 2	Immediate Group 3	Delayed Group 1	Delayed Group 2	Delayed Group 3
1						
2	W23221	W23222	W23223	W23224	W23225	W23226
3						
4						
5	W23221	W23222	W23223	W23224	W23225	W23226
6						
7						
8	W23221	W23222	W23223	W23224	W23225	W23226
9						
10						
11	W23221	W23222	W23223	W23224	W23225	W23226
12						
13						
14	W23221	W23222	W23223	W23224	W23225	W23226
15						
16						
17	W23221	W23222	W23223	W23224	W23225	W23226
18						



**Example 1:** 2 Group 1 Immediate Outputs, 1 Group 2 Immediate Outputs, 2 Group 3 Immediate Outputs:  
**Example 2:** 1 Group 1 Immediate Outputs, 2 Group 3 Immediate Outputs, 1 Group 1 Delayed Outputs:  
**Example 3:** 1 Group 2 Immediate Outputs, 2 Group 3 Immediate Outputs, 3 Group 3 Delayed Outputs:

2	1	2	0	0	0
1	0	2	1	0	0
0	1	2	0	0	3

5  
4  
6  
Total Modules